HEWLETT-PACKARD COMPANY Intellectual Property Administration PP.CO. Box 272400

Fort Callins, Colorado 80527-2400

PATENT APPLICATION

ATTORNEY DOCKET NO. __20031967-1

ËNT AND TRADEMARK OFFICE

Andrej Kocev et al.

Confirmation No.: 1813

Application No.: 09/944,776

Examiner: Pham, Thomas K.

2121

Filing Date:

August 31, 2001

Group Art Unit:

Title:

Sir:

Programmable Tuning for Flow Control and Support for CPU Hot Plug

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Response/Amendment

RECEIVED

JUL 0 8 2004

Technology Center 2100

Petition to extend time to respond

TRANSMITTA	LLEIIEN	FUN NESPL	JINOE/AIVIEN	IDMENT

Transmitted herewith is/are the following in the above-identified application:

Other:						CMALL	CAITIT		(fee \$_		
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT				4) NUMBER	(5) PRESENT		(6) RATE		(7) ADDITIONAL FEES	
TOTAL CLAIMS			MINUS				0	×	\$18	\$	0
INDEP. CLAIMS			MINUS			=	0	х	\$86	\$	0
[] FIR:	ST PRESENTATION	OF A	MULTIPLE	DEPENDENT	CLAIM			+	\$290	\$	0
EXTENSION FEE	1ST MO \$110.0			MONTH 3RD MON 20.00 \$950.00				1 MON 480.0		\$	0
						•	0	THER	FEES	\$	
				TOTAL A	DDITIONAL FE	E FOR	THIS A	MEND	MENT	\$	0

to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

Date of Deposit: July 1, 2004

Typed Name: Melissa L. Altman

Signature:

Respectfully submitted,

Andrej Kocev et al.

Michael R. Reinemann

Attorney/Agent for Applicant(s)

Reg. No. 38,280

Date: July 1, 2004

Telephone No.: 617 951-2500

Rev 10/03 (TransAmd)